



# Research and Development Section

## Indian Institute of Technology Guwahati

### Guwahati-781039, Assam

Applications are invited for a **Walk-in mode interview** for the following post(s) in the project entitled, "**Developing a post quantum lattice based block cipher**" at the Department of Electronics and Electrical Engineering, IIT Guwahati.

**Date: 13 August 2024 (Tuesday)**

**Time: 3.00 pm**

**Venue: At E&ICT Academy, Technology Complex, IIT Guwahati-781039**

Sl. No.	Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA (Rs.)	Medical (Rs.)	Total Amount (Rs.)	Duration of Appointment in Months	Qualifications
1	SRF DIRECT	03	35,000	6,300	1250	42,550	8 Months	Post Graduate in Electronics and Electrical Engineering / Electronics and Communication Engineering / Computer Science Engineering / Information Technology

**How to apply and selection process:** Interested candidates should apply by filling out the Online application form given below before **10th August 2024 (16:00 Hrs)**. Please note that the candidates who are already employed under Central/State Govt./PSU/Autonomous Bodies/Private Organizations etc. will have to submit a No-objection Certificate (NOC) from the concerned employer in advance or at the time of the interview failing which the candidate will not be allowed to appear for an interview. Shortlisted candidates will be informed via E-mail by **11th August 2024 (05:00 hrs)**

**Link to apply:** <https://forms.gle/NXvUG6JHs54R9kF3A>

Selection will be based on the performance of the candidate in the walk-in interview. Candidates will not be sent any call letter separately.

For any clarification, contact: Dr.Srinivasan Krishnaswamy (Principal Investigator)

Email: [srinikris@iitg.ac.in](mailto:srinikris@iitg.ac.in)

Phone: +91 361 2582526

**No TA/DA will be paid to the candidates for appearing in the test and interview**

**xEEESPNTAR00934xxSK001- 3668- STARS**

**Assistant Registrar (R&D)**