



# Research and Development Section Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for an **online interview** for the following post in a CEFIPRA funded project entitled, “Machine-Learning Enabled Next-Generation IoT Communications” at the Department of Electronics and Electrical Engineering, IIT Guwahati.

**Date: 5 March 2025 (Wednesday),**

**Time: 10 am onwards**

**Online Interview: Link will be provided to the shortlisted candidates**

Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA Required (Rs.)	Total Amount (Rs.)	Duration of Appointment in days	Qualifications
Junior Research Fellow	1	With GATE Score			89	BE/B.Tech or ME/ M.Tech or MSc.in relevant area  Clearing the GATE Exam is not mandatory. However, candidates without GATE will be considered for the lower pay grade.
		37,000	6660	43,660		
		Without GATE Score				
		18,600	3348	21,948		

**How to apply and selection process:** Eligible candidates have to apply with a CV/Resume including details of all educational qualifications, experience (if any), contact address, phone no., E-mail etc. along with scanned copies of relevant documents at email address ([kuntaldeka@iitg.ac.in](mailto:kuntaldeka@iitg.ac.in)) (with subject: Application for the JRF Recruitment) before **4 March 2025** (1700 Hrs). Shortlisted candidates will be called for an Online Interview on **5 March 2025** (Wednesday) at **10 AM** onwards. Selection will be based on the performance of the candidate in the interview.

Note:

1. Only shortlisted candidates will be sent call letters.
2. No TA/DA will be paid to the applicants for appearing in the online interview.
3. Recruitment of selected candidate will be subjected to verification of original certificates.

For any clarification, contact:

Dr. Kuntal Deka (Principal Investigator)

Department of Electronics and Electrical Engineering, IIT Guwahati

Email: [kuntaldeka@iitg.ac.in](mailto:kuntaldeka@iitg.ac.in); Phone: +918638304706

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Assistant Registrar (R&D)